

New Divisional Application
Attorney Docket No. 240029US-2DIV
Preliminary Amendment filed herewith
Inventor: Yasushi Sasaki et al.

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claims 1-3 (Canceled)

Claim 4 (Original): A pumping circuit, comprising:

a first transistor having a first electrode region and a third electrode region, which are supplied with a power source voltage, and a second electrode region connected to a potential of a first intermediate node;

a second transistor having a first electrode region supplied with a power source voltage, a second electrode region connected to a potential of a second intermediate node and a third electrode region connected to the potential of said first intermediate node; and

a third transistor having a first electrode region connected to the potential of said second intermediate node, a second electrode region connected to an output portion together with a third capacitor, and a third electrode region connected to the potential of said first intermediate node,

wherein a first clock input portion for receiving a first clock signal through a first capacitor is connected to said first intermediate node, and a second clock input portion for receiving a second clock signal through a second capacitor is connected to said second intermediate node.

Claim 5 (Original): The pumping circuit according to Claim 4, wherein said first transistor and said second transistor are the ones of the same polarity, and said third transistor is the one of an opposite polarity to that of said first and second transistors.

Claim 6 (Original): The pumping circuit according to Claim 4, wherein said second clock signal is an inverted clock signal obtained from said first clock signal.

Claim 7 (Original): A pumping circuit, wherein first and second transistors having opposite polarities are connected in series and two transistor pairs composed of the first and second transistors are connected in series.

Claim 8 (Original): A pumping circuit, wherein first and second transistors having opposite polarities are connected in parallel and two transistor pairs composed of the first and second transistors are connected in series.

Claim 9 (Original): A pumping circuit, comprising:

a first pumping circuit in which a first transistor of a first conductivity type and a second transistor of a second conductivity type are connected in series, and two transistor pairs composed of the first and second transistors are in series connected;

a second pumping circuit in which a first transistor of the second conductivity type and a second transistor of the first conductivity type are connected in series, and transistor pairs composed of the first and second transistors are in series connected; and

an operational amplifier which receives an output from said first pumping circuit as a positive polarity power source and an output from said second pumping circuit as a negative polarity power source.

Claims 10-19 (Canceled)